

Advertisement for the post of Junior Research Fellow in SERB funded Research Project

Applications are invited from Indian nationals for the position of Junior Research Fellow (JRF) in a SERB funded research project with the following details:

Title of the Project: A fully integrated low-latency, high dynamic range, bio-inspired event-based dynamic and active vision sensor (DAVIS) with global shutter operation for object tracking, classification, and recognition in a highly dynamic scene

Principal Investigator: Dr. Kapil Jainwal, Assistant Professor, Dept. of Electrical Engineering, IIT Hyderabad (kapiljainwal@ee.iith.ac.in). _

Name of the position: Junior Research Fellow (JRF)

Number of Positions: One

Essential qualifications:

Candidates need to satisfy the following two criteria:

1. Graduate in Electronics/Communications/Instrumentation with at least 1 years of industry/research experience in the domain of Electronic Systems/VLSI/Embedded Systems **or** M.Tech/ME/MS in Electronics/Communication/Instrumentation/VLSI/Microelectronics/Electronics SystemDesign/Embedded Systems/ or related areas.
2. Qualified in any of the following National eligibility test/exam:
 - a. GATE
 - b. CSIR-UGC NET including lectureship (Assistant Professorship)
 - c. National-level examination conducted by Central Govt. Departments and their agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER, etc.

Desirable:

- Having good knowledge of Circuit Design, Analog/Digital Circuits, Layout Design, and Electronic Devices.
- Programming skills in C/Python/MATLAB.

Age limit: 35 Years

Salary: Rs. 31,000/- + 24% House Rent Allowance per month

Duration: The appointment for the above contractual position will be initially for a period of 01 year, which may be further extended or curtailed, based on the performance of the candidate, availability of funds in the project, and requirement in the project.

How to apply: Interested candidates must apply on the application form attached with this advertisement. Duly completed application form along with candidate's detailed CV must be mailed

to the PI, Dr. Kapil Jainwal at kapiljainwal@ee.iith.ac.in so as to reach him by **31/11/2023**. Only shortlisted candidates will be called for interview. The position is available immediately.

Terms & Conditions:

- i. No TA/DA will be provided to the candidate for the interview
- ii. The PI shall not be responsible for postal/courier delay if any, or any other reason for non-receipt of the document at the specified time and will result in disqualification/rejection of the application.
- iii. The decision of the selection committee will be final.
- iv. If the number of shortlisted candidates called for the interview is large, the selection committee may decide to restrict the number of candidates for the interview to a reasonable limit after considering qualifications and experience over and above the minimum prescribed in the advertisement.
- v. The appointment of the candidate will be governed by the terms and conditions of the Institute/ funding agency particularly applicable to the said project as and when required.
- vi. The selected candidate will have to join duty immediately on receipt of the offer.
- vii. The fellowship may be terminated with a 30-day notice before completion of the tenure if performance till date is not deemed satisfactory.

Please feel free to contact me in case of any query (Principal Investigator)

Dr. Kapil Jainwal
Assistant Professor
Department of Electrical Engineering
IIT Hyderabad
Email: kapiljainwal@ee.iith.ac.in