Birla Institute of Technology & Science, Pilani
Hyderabad Campus

Dt. 01.05.2023

Call for Project Research Positions for DST-PURSE Project

Applications are invited for the post of **Project Associate-I**

(Previously termed as JRF, please refer to Annexure-I for details)

Project Title : Promotion of University Research and Scientific Excellence (PURSE) (Development of optimized Electrical Interface System for kW scale Fuel cell stack with dynamic load bank)

Funding Agencies: Department of Science and Technology (DST), Government of India and BITS Pilani

Reference : DST-OM 42(02)/PF-II/2014 dated 06.08.2020

Project Location : BITS Pilani, Hyderabad Campus

Duration : 3 Years

Position: Project Associate-I

Fellowship: Rs. 31,000/ month for candidates who have qualified for GATE or CSIR/UGC-JRF/NET. Candidates who have not qualified for the above conditions are entitled to a fellowship of **Rs. 25,000**/ month.

Essential Qualification: M.E/M.Tech in Electrical Engineering / Energy/ Renewable Energy from a recognized University.

Desirable Qualification: M.E/M.Tech in Power Electronics

Expertise: Must have hands-on experience in design (simulation) and development of Power Electronic converter, inverter (grid-tied and stand-alone) circuits, protection of circuits, use of Microcontroller, and PCB design.

Interested candidates should apply by filling out this form https://forms.gle/ubGLASFP3ew8QiLP7 on or before **15.05.2023 (Monday)**

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A **Google meet link** to attend the online interview will be sent to the eligible candidates in due course of time.

Selected candidates may get the additional opportunity to be enrolled for the **Ph.D** program of BITS Pilani, Hyderabad Campus, based on the institutional rules and regulations.

Investigator Details	**Dr. Ankur Bhattacharjee**,
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Dr. Ankur Bhattacharjee
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