



# Birla Institute of Technology & Science, Pilani Hyderabad Campus

Dr. Chetan Kumar Vudadha, Assistant Professor, EEE

## **CALL FOR JUNIOR RESEARCH FELLOW (JRF) FOR A DST-SERB PROJECT**

Date: 27<sup>th</sup> January, 2022

Applications are invited from suitable candidates for a **Junior Research Fellow (JRF)** position to work on the following project sponsored by **Science and Engineering Research Board (SERB)** having No. **SRG/2020/000202** under the supervision of **Dr. Chetan Kumar Vudadha, Assistant Professor, Department of Electrical and Electronics Engineering, BITS-Pilani, Hyderabad campus.**

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<b>Title of the project:</b>	A Novel 2:1 Multiplexer based Approach to Implement Ternary Logic Circuits
<b>Duration of the project:</b>	11 Months
<b>Project position:</b>	Junior Research Fellow (JRF)
<b>Number of Positions:</b>	One
<b>Fellowship amount:</b>	31,000/- per month
<b>Eligibility requirements:</b>	M.E. / M. Tech (in VLSI, Microelectronics or allied areas). <b>GATE</b> Qualification is mandatory.
<b>Desired Experience:</b>	Prior exposure/experience in SPICE level coding and simulation of VLSI circuits is desirable

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**Interested candidates with the above mentioned qualification can fill details and upload their CV in the google form (<https://forms.gle/WYcaY5XsgTXnbdU97>) by 10<sup>th</sup> February 2022 5PM.** The CV should contain detailed information about the educational qualifications, research experience and published papers (if any), photograph and scanned copies of documents including- support of date of birth, educational qualifications (BE/B.Tech, ME/M.Tech), experience certificates from prior employment/project work (if any).

Please note that only qualified and suitable candidates will be notified for Interview via email. Interview would be through Google Meet.

Selected candidate may be permitted to register for Ph.D. program of BITS, Pilani, subject to the fulfilment of the requirements.

Best Regards,  
Dr. Chetan Kumar V  
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