

Advertisement for the position of Design Engineer in a DST funded project

Date: 15th March, 2021

Applications are invited from bright candidates for Design Engineer in a project funded by DST.

Minimum qualification:

- PhD in EE/ECE with specialization in Semiconductor Devices with excellent publication record.
- Prior knowledge of nanomaterials based composites for electronic applications and product development/prototyping

Emoluments: 35000-38000 INR consolidated (no HRA)

Duration of the project: 6 months (subsequent extension based on performance)

How to apply: Eligible candidates may send their CV to fendlabiith@gmail.com on/before **March 20th, 2021** with the subject title “For the position of Design Engineer”.

Shortlisted candidates will be informed over email by **March 21st, 2021**.

Shortlisted candidates will have to appear for an online interview tentatively on/before **March 23rd, 2021**. Candidates will be required to join latest by **April 7th, 2021**.

Note: No TA/DA is admissible for attending the interview.